

POTENTIAL INDUCED DEGRADATION OF CRYSTALLINE SILICONE CELLS WITH VARIOUS ANTIREFLECTIVE COATINGS

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Abstract/Summary:

The first experiences with potential induced degradation (PID) of silicon solar cells have been presented in literature since 1989 [1-4]. It has been shown that PID may cause power losses of more than 30% for modules out in the field. Critical issues on the cell level are the SiNx anti-reflective coatings (ARC), their deposition method, the emitter thickness and metallization (thus influencing the electrical series resistance). The leakage current was found as an indicator for the intensity of degradation. Moreover, different test methods to prove the stability or susceptibility of samples have been presented. In this paper we are showing the influence of the anti-reflection coating to provoke PID and the ability of silicon nitride layers to prevent PID. For each layer structure, at least four different designs have been manufactured and tested for their stability against PID. To do so, it was necessary to find an appropriate test sequence, which would show polarization effects in an adequate time frame, while there is no standard procedure available that addresses this problem for the time being. Furthermore, it has been necessary to develop suitable reference samples to compare the results and to prove the repeatability of the test sequences. The experiment has been divided into two parts: The pre-evaluation has been realized with small onecell mini-modules to probe the adequate testing parameters and to analyze the leakage currents for various prone samples. During the second part, we also investigated the reversibility of PID - depending on the state of degradation of these samples - to check whether PID is one 100% reversible, or whether the degradation mechanism also shows an irreversible part.

For more Information on the topic please contact the R&D Team of PI Berlin.

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POTENTIAL INDUCED DEGRADATION EFFECTS ON CRYSTALLINE SILICON CELLS WITH VARIOUS ANTIREFLECTIVE COATINGS



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→ Introduction

The polarization of crystalline silicon solar cells is another aspect of potential induced degradation (PID) effects. The presence of a TCO coating at thin film modules. Both effects are induced by sodium diffusion from the glass through the encapsulant. For crystalline silicon it can cause power losses in the field of more than 10% [1]. In this paper, we are showing the influence of the anti-reflection coating to provoke PID and the ability of nitride layers to prevent PID. The experiment has been divided into two parts. The first part is to investigate the hot spot risk and the reversibility of the leakage currents for various porous samples. During the second part, we also investigated the hot spot risk and the reversibility of the degradation mechanism, also shows an irreversible part.

Three different PID tests

- (1) Climate condition of 85°C with a relative humidity of 65% for 48 hours (internal P1-Berlin standard). This PID test shows the fastest degradation.
- (2) Climate condition of 60°C with a relative humidity of 65%, for 168 hours (IEC standard). Shows the best results for the P1 standard test but after 168 hours instead of 336 hours.
- (3) Climate condition of 25°C with no specific humidity level compo for 168 hours combined with the PID test and copper foil on published during PVSEC2011). This test shows the same results after 50 cycles a 168 hours.

All tests with negative system voltage against ground and fully contacted with metal foil on the front glass.

Silicon nitride refractive index

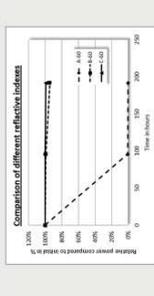


Fig. 1: PID results after two cycles of PID/60/65 of cells with different refractive index (A=2.03; B=2.2; C=2.3).

Double and triple layer structures

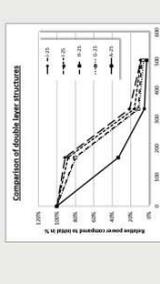


Fig. 3: Different degradation behaviors of various double layer cells (J-G) compared to a standard single layer (A).

Modified layers

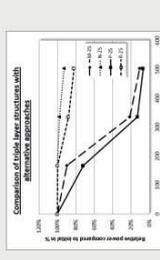


Fig. 4: Cells with triple layer with (N, R) and without (M, P) alternative approaches during the (168h)-25°C-test-method.

Hot Spot Risk

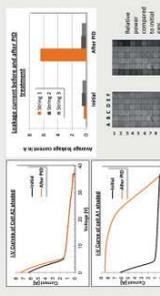


Fig. 6: Hot Spot risk analysis before and after PID treatment.

Recovery levels

Temperature level	Below 15%	Above 15%
Time of determination	PID level: 6.45%, DSE: 81.58%	PID level: 62.95%, DSE: 95.72%
60°C	0.68% / 74.83%	52.70% / 95.05%
85°C	0.95% / 74.82%	61.48% / 97.50%

Fig. 5: Different PID behaviour of cells with modified ARC deposition, compared with the best performing standard triple layer. M (see Fig. 4) during the 168h-25°C-test-cycle.

Comparison between double and triple layer structures

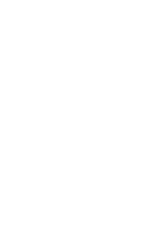


Fig. 8: Comparison between PID degradation and PID prevention of different cell types (A-F) under different climatic conditions summarized in two groups, one with power under (left diagrams) and over (right diagrams) 15% of the initial power.

Conclusion

The SiNx anti reflective coating has significant influence on the PID effect. The use of a third ARC layer, no further improvement could be monitored. All in all, adding multi-layer-structures to the cells shows an improvement in the hot spot risk and the PID effect. An alternative approach to modify the ARC coating to reduce the PID effect were considerably more effective. Further experiments will continue and modify the investigated approaches to gain more insight into the PID mechanism. It was shown that PID affected cells can be made a higher risk of hot-spotting. It was shown that the 85°C test is two times as burdensome as the 60°C and under 15% to 55%. Finally, it was observed that the degradation is not completely recoverable depending on the degradation level of the module.

Fig. 7: Increase of the temperature difference between minimum and maximum temperatures during PID phase (1-20) and PID (21-60) cells during a hot spot analysis.



Fig. 8: Comparison between PID degradation and PID prevention of different cell types (A-F) under different climatic conditions summarized in two groups, one with power under (left diagrams) and over (right diagrams) 15% of the initial power.

